

09/784221

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[illegible]

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

SCANNED

**Q.A.**

APPLICATION NO.  
(09/784221

**CONT/PRIOR**

**CLASS**

SUBCLASS<sup>2</sup>

## ART UNIT

**EXAMINER**

PHU

TS 100 Alexander C. Leyn

Method and system for distribution of clock and frame  
synchronization information

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) _____ (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	

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(FACE)